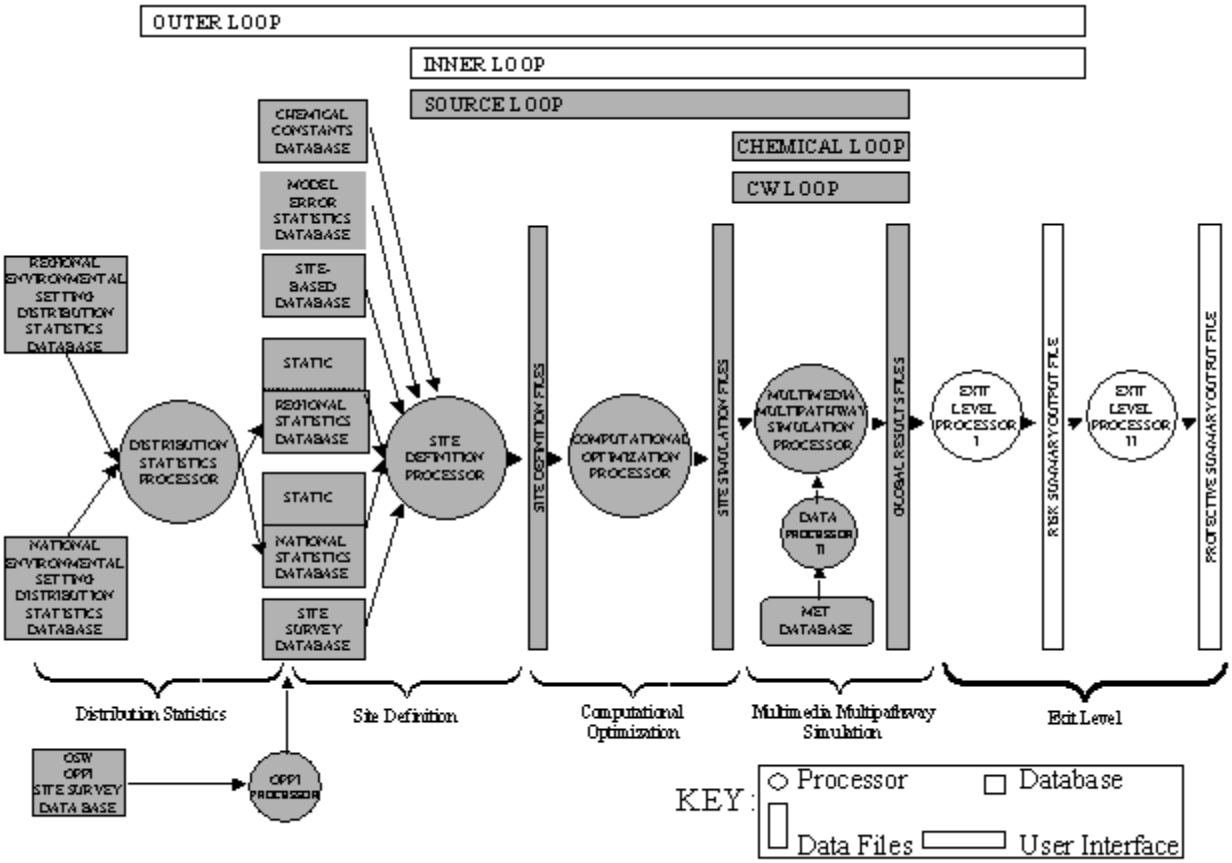


Figure 8.18 View of the Exit Level Processors

Figure 3.21 View of exit level processor.